Ckingway

SPECIFICATION

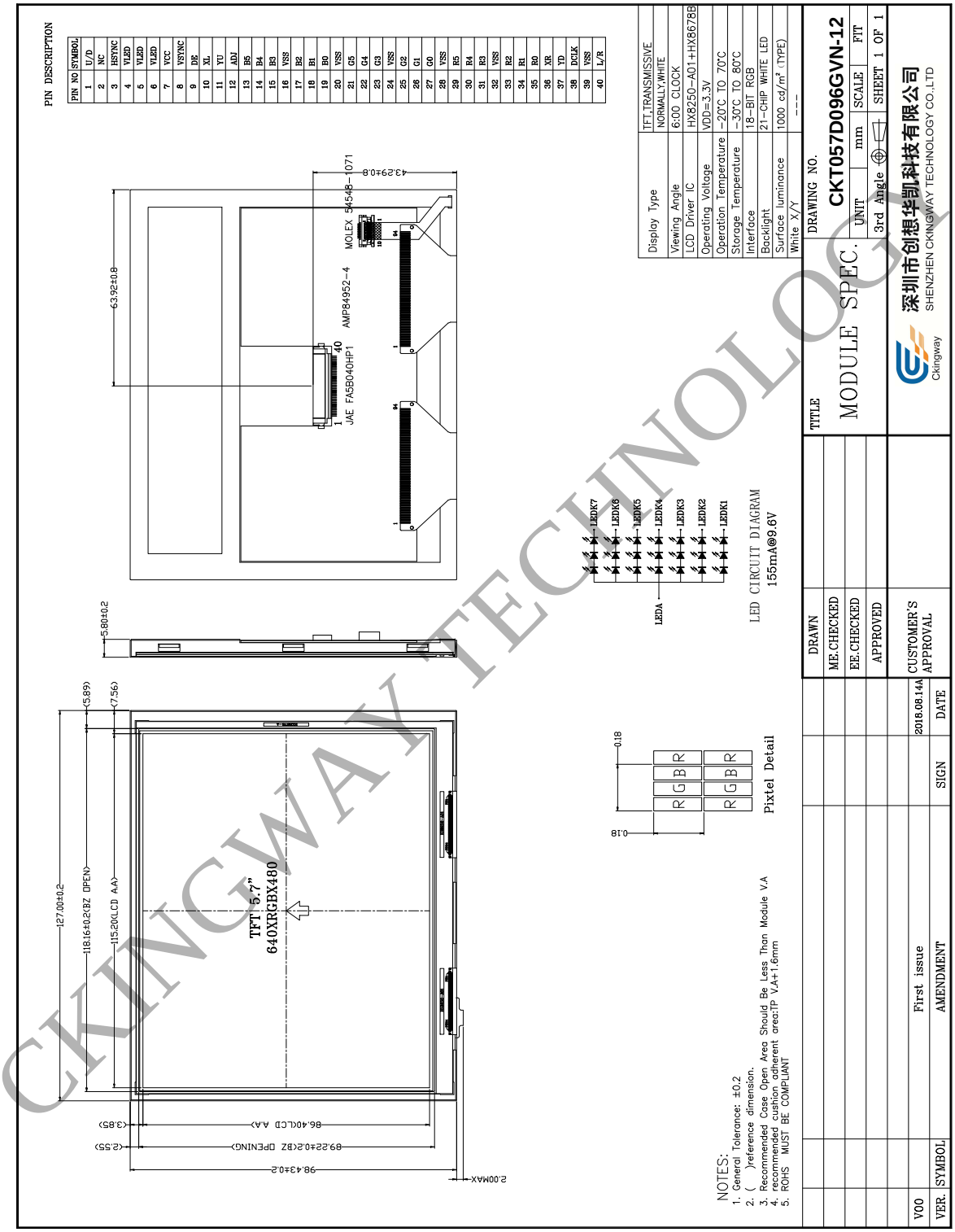
LCD Number: CKT057D096GVN-12

CUSTOMER APPROVED	**PREPARE BY**	**CHECK BY**	**APPROVED BY**
SUPPLIER APPROVED	**PREPARE BY**	**CHECK BY**	**APPROVED BY**

1. General Specification

Item	Contents	Unit
LCD TYPE	TFT/TRANSMISSIVE	
MODULE SIZE (W*H*T)	127.00*98.43*9.2(TOTAL)	MM
ACTIVE SIZE (W*H)	115.20*86.40	MM
PIXEL PITCH (W*H)	0.18*0.18	MM
NUMBER OF DOTS	640*480	
DRIVER IC	HX8250-A+HX8678-B	
INTERFACE TYPE	18-BIT RGB	
TOP POLARIZER TYPE	GLARE	
RECOMMEND VIEWING DIRECTION	6	O'CLOCK
GRAY SCALE INVERSION DIRECTION	12	O'CLOCK
BACKLIGHT TYPE	21-DIES WHITE LED	
TOUCH PANEL TYPE	WITHOUT	

2. Mechanical Drawing



4. Interface Pin Function

Pin No.	Symbol	Description
1	U/D	Up/down scan setting. When U/D=H, reverse scan. When U/D=L, normal scan.
2	NC	No connection.
3	HSYNC	Horizontal sync input in digital RGB and CCIR601 mode. (Short to GND if not used)
4	VLED	Power supply for BLU LDO circuit.
5	VLED	Power supply for BLU LDO circuit.
6	VLED	Power supply for BLU LDO circuit.
7	VCC	Power supply.
8	VSYNC	Vertical sync input in digital RGB and CCIR601 mode. (Short to GND if not used)
9	DE	Input data enable control. When DE mode, active High to enable data input. Default pull low.
10	NC(X2)	No connection.(Touch panel control PIN: X2)
11	NC(Y1)	No connection.(Touch panel control PIN: Y1)
12	ADJ	Chip Enable (Active High).
13	B5	Blue data input.
14	B4	Blue data input
15	B3	Blue data input
16	VSS	Power ground.
17	B2	Blue data input.
18	B1	Blue data input.
19	B0	Blue data input.
20	VSS	Power ground.
21	G5	Green data input.
22	G4	Green data input.
23	G3	Green data input.
24	VSS	Power ground.
25	G2	Green data input.
26	G1	Green data input.
27	G0	Green data input.
28	VSS	Power ground.
29	R5	Red data input.
30	R4	Red data input.
31	R3	Red data input.
32	VSS	Power ground.
33	R2	Red data input.
34	R1	Red data input.
35	R0	Red data input.
36	NC(X1)	No connection.(Touch panel control PIN: X1)
37	NC(Y2)	No connection.(Touch panel control PIN: Y2)

38	DCLK	Clock signal. Latching data at the rising edge.
39	VSS	Power ground.
40	L/R	The shift direction of device internal shift register is controlled by this pin as shown below: L/R=H: STH->SO1->• • •->SO960->STHO L/R=L: STH->SO960->• • •->SO1->STHO