Ckingway

SPECIFICATION

LCD Number: CKT013W101STN-G03

CUSTOMER APPROVED	**PREPARE BY**	**CHECK BY**	**APPROVED BY**
SUPPLIER APPROVED	**PREPARE BY**	**CHECK BY**	**APPROVED BY**

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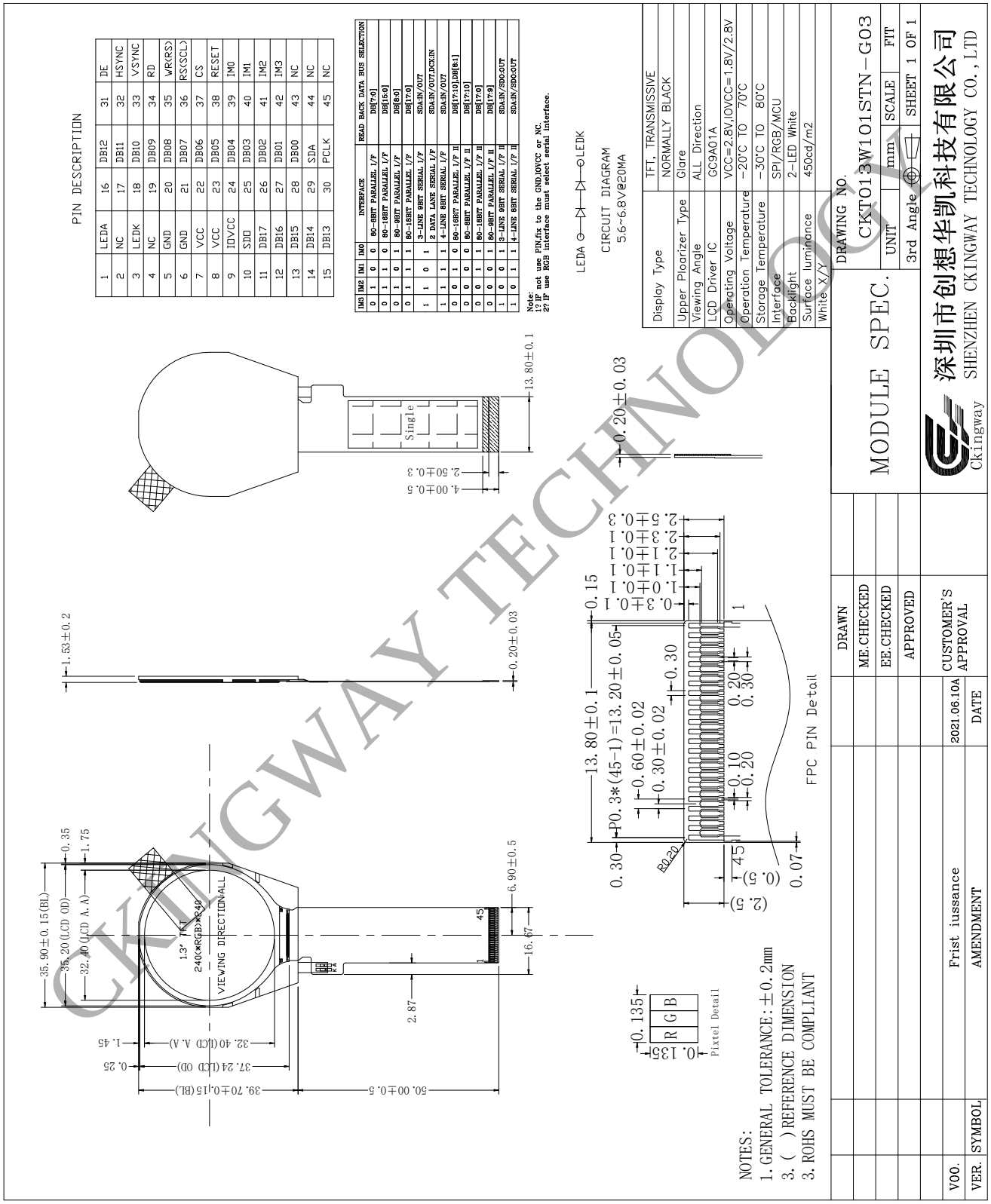
1. General Specification

Item	Contents	Unit
LCD TYPE	TFT/TRANSMISSIVE	
MODULE SIZE (W*H*T)	35.90*39.70*1.53	MM
ACTIVE SIZE (W*H)	23.40*23.40	MM
PIXEL PITCH (W*H)	0.135*0.135	MM
NUMBER OF DOTS	240*240	
DRIVER IC	GC9A01A	
INTERFACE TYPE	SPI/RGB/MCU	
TOP POLARIZER TYPE	ANTI-GLARE	
RECOMMEND VIEWING DIRECTION	ALL	O'CLOCK
GRAY SCALE INVERSION DIRECTION	-	O'CLOCK
BACKLIGHT TYPE	2-DIES WHITE LED	
TOUCH PANEL TYPE	WITHOUT	

2. Mechanical Drawing



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4. Interface Pin Function

Pin No.	Symbol	Description
1	LEDA	Anode of LED backlight.
2	NC	No connection.
3	LEDK	Cathode of LED backlight.
4	NC	No connection.
5	GND	Power ground.
6	GND	Power ground.
7	VCC	Supply Voltage.
8	VCC	Supply Voltage.
9	IOVCC	IO Voltage.
10	SDO	Serial output signal. The data is outputted on the falling edge of the SCL signal.
11	DB17	Data bus.
12	DB16	Data bus.
13	DB15	Data bus.
14	DB14	Data bus.
15	DB13	Data bus.
16	DB12	Data bus.
17	DB11	Data bus.
18	DB10	Data bus.
19	DB09	Data bus.
20	DB08	Data bus.
21	DB07	Data bus.
22	DB06	Data bus.
23	DB05	Data bus.
24	DB04	Data bus.
25	DB03	Data bus.
26	DB02	Data bus.
27	DB01	Data bus.
28	DB00	Data bus.
29	SDA	When IM[3]:Low, Serial in/out signal in 3-wire 9-bit/4-wire 8-bit serial data interface. When IM[3]:High, Serial input signal in 3-wire 9-bit/4-wire 8-bit serial data interface. The data is applied on the rising edge of the SCL signal.
30	PCLK	Dot clock signal for RGB interface operation.
31	DE	Data enable signal for RGB interface operation.
32	HSYNC	Line synchronizing signal for RGB interface operation.
33	VSYNC	Frame synchronizing signal for RGB interface operation.
34	RD	8080-I/8080-II system (RDX): Serves as a read signal and MCU read data at the rising edge.
35	WR(RS)	8080-I/8080-II system (WRX): Serves as a write signal and writes data at the rising edge. 4-line system (D/CX): Serves as command or parameter select.
36	RS(SCL)	This pin is used to select "Data or Command" in the parallel interface. This pin is used serial interface clock in 3-wire 9-bit / 4-wire 8-bit serial data interface.

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37	CS	Chip select input pin("Low" enable).
38	RESET	This signal will reset the device and must be applied to properly initialize the chip. Signal is active low.
39	IM0	Select the MCU interface mode
40	IM1	Select the MCU interface mode
41	IM2	Select the MCU interface mode
42	IM3	Select the MCU interface mode
43	NC	No connection.
44	NC	No connection.
45	NC	No connection.

Note:Select the MCU interface mode MPU Parallel interface bus and serial interface select If use RGB Interface must select serial interface.