



# SPECIFICATION

LCD Number: CKT026M038LQT-A1

CUSTOMER APPROVED	PREPARE BY	CHECK BY	APPROVED BY
SUPPLIER APPROVED	PREPARE BY	CHECK BY	APPROVED BY

# 1. General Specification

Item	Contents	Unit
LCD TYPE	TFT/TRANSMISSIVE	
MODULE SIZE (W*H*T)	46.00*69.00*4.30	MM
ACTIVE SIZE (W*H)	39.60*52.80	MM
PIXEL PITCH (W*H)	0.165*0.165	MM
NUMBER OF DOTS	240*320	
DIVER IC	ILI9341V	
INTERFACE TYPE	MCU RGB+SPI	
TOP POLARIZER TYPE	ANTI-GLARE	
RECOMMEND VIEWING DIRECTION	ALL	O'CLOCK
GRAY SCALE INVERSION DIRECTION	-	O'CLOCK
BACKLIGHT TYPE	6-DIES WHITE LED	
TOUCH PANEL TYPE	RESISTIVE	

# 2. Mechanical Drawing

**Pin Description Table:**

PIN NO	SYMBOL	DESCRIPTION
1	GND	
2	VCI	
3	VCI	
4	IN2	
5	IN2	
6	IN1	
7	RESST	
8	CS	
9	RES(SC1)	
10	WR(D/C)	
11	RD	
12	VSYNC	
13	HSYNC	
14	ENABLE	
15	POCLK	
16	POCLK	
17	DR0	
18	DR1	
19	DR2	
20	DR3	
21	DR4	
22	DR5	
23	DR6	
24	DR7	
25	DR8	
26	DR9	
27	DR10	
28	DR11	
29	DR12	
30	DR13	
31	DR14	
32	DR15	
33	DR16	
34	YU	
35	XL	
36	YU	
37	YU	
38	YU	
39	YU	
40	GND	
41	LEDK5	
42	LEDK6	
43	XR	
44	YU	
45	XL	
46	YU	
47	YU	
48	GND	
49	GND	
50	GND	

**MCU-Interface Mode:**

MCU-Interface Mode	DR0	DR1	DR2	DR3	DR4	DR5	DR6	DR7	DR8	DR9	DR10	DR11	DR12	DR13	DR14	DR15	DR16
0 0 0	80	MCU 8-bit bus	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70
0 1 0	80	MCU 8-bit bus	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70
0 1 1	80	MCU 18-bit bus	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70	DR70
1 0 1	13	Wire 8-bit data serial	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT
1 1 0	14	Wire 8-bit data serial	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT	SDA1N/OUT

**LED Circuit Diagram:**

```

    LEDA6 --- LEDK1
              LEDK2
              LEDK3
              LEDK4
              LEDK5
              LEDK6
  
```

**Notes:**

- General Tolerance: ±0.2
- ( ) reference dimension.
- Recommended Case Open Area Should Be Less Than Module VA
- recommended cushion coherent area: TP\_VA+1.6mm
- ROHS MUST BE COMPLIANT

**Module Specifications:**

UNIT	SCALE	FIT
3rd Angle		SHDET 1 OF 1

**Display Type:** TFT  
**Optimum Viewing Direction:** ALL VIEWING  
**Upper Polarizer Type:** Anti-Glare  
**LCD Driver IC:** ILI9341V  
**Operating Voltage:** VCI=2.8V, IOVCC=1.8V/2.8V  
**Operation Temperature:** -20°C TO 70°C  
**Storage Temperature:** -30°C TO 80°C  
**Interface:** TYPE II  
**Backlight:** 6-CHIP WHITE LED  
**Surface luminance:** 280 cd/m<sup>2</sup> (TYP.)  
**White X/Y:** ---

**Pin No. Description Table:**

Pin No.	Description
1	YD
2	XL
3	YU
4	XR

**LED Circuit Diagram:**  
2, 8-3, 4V@120mA

**Module Specifications Table:**

TITLE	DRAWING NO.
MODULE SPEC.	CKT026M038LQT-A1

**Revision History:**

VER.	SYMBOL	AMENDMENT	SIGN	DATE
V04		增加排线丝印		2017.5.20
V03		更新易撕膜位置和下沉位置的尺寸公差		2017.3.19
V02		背光添加LCD液晶封口避空区		2017.2.7
V01		更换新开背光		2016.12.01
V00		First issue		2016.04.06

**Customer Approval:**

APPROVED: \_\_\_\_\_  
CUSTOMER'S APPROVAL: \_\_\_\_\_

**Company Information:**  
 深圳市创想华凯科技有限公司  
 SHENZHEN KINGWAY TECHNOLOGY CO.,LTD  
 Kingway

### 3. Interface Pin Function

Pin No.	Symbol	Description
1	GND	Power ground.
2	VCI	Power supply.
3	VCI	Power supply.
4	IM2	Select the MCU interface mode.(Note)
5	IM1	
6	IM0	
7	RESET	Reset pin
8	CS	Chip select input pin ( “Low” enable).
9	RS(SCL)	Data or command select
10	WR(D/C)	Write data signal
11	RD	Read data signal; negative polarity.
12	VSYNC	Vertical sync signal; negative polarity.
13	HSUNC	Horizontal sync signal
14	ENABLE	Data input enable. Active High to enable the data input.
15	DOTCLK	Clock signal; latching data at the falling edge.
16	SDA	Serial Data.
17	DB0	Data bus
18	DB1	Data bus
19	DB2	Data bus
20	DB3	Data bus
21	DB4	Data bus
22	DB5	Data bus
23	DB6	Data bus
24	DB7	Data bus
25	DB8	Data bus
26	DB9	Data bus
27	DB10	Data bus
28	DB11	Data bus
29	DB12	Data bus
30	DB13	Data bus
31	DB14	Data bus
32	DB15	Data bus
33	DB16	Data bus
34	DB17	Data bus
35	NC	No connect
36	LEDA	Anode of LED backlight
37	LEDK1	Cathode of LED backlight
38	LEDK2	Cathode of LED backlight
39	LEDK3	Cathode of LED backlight
40	LEDK4	Cathode of LED backlight

41	LEDK5	Cathode of LED backlight
42	LEDK6	Cathode of LED backlight
43	XR	TP pin.
44	YU	TP pin.
45	XL	TP pin.
46	YD	TP pin.
47	GND	Power ground.
48	GND	Power ground.
49	GND	Power ground.
50	GND	Power ground.

Note:

IM2	IM1	IM0	MCU-Interface mode	DB pin in use	
				Register/Content	GRAM
0	0	0	80 MCU-8bit bus	D[7:0]	D[7:0]
0	0	1	80 MCU-16bit bus	D[7:0]	D[15:0]
0	1	0	80 MCU-9bit bus	D[7:0]	D[8:0]
0	1	1	80 MCU-18bit bus	D[7:0]	D[17:0]
1	0	1	3-Wire 9-bit data serial	SDA:IN/OUT	
1	1	0	4-Wire 8-bit data serial	SDA:IN/OUT	